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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/697,015	10/31/2003	Yoshinori Shizuno	OHG 140	8235
23995	7590	03/10/2006	EXAMINER	
RABIN & Berdo, PC 1101 14TH STREET, NW SUITE 500 WASHINGTON, DC 20005			PAREKH, NITIN	
			ART UNIT	PAPER NUMBER
			2811	

DATE MAILED: 03/10/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Supp
Notice of Allowability

Application No.

10/697,015

Examiner

Nitin Parekh

Applicant(s)

SHIZUNO, YOSHINORI

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 06-24-05.
2. ☒ The allowed claim(s) is/are 1-4, 6, 7, 18-20 and 22-24.
3. ☒ The drawings filed on 06 June 2005 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

Nitin Parekh
Nitin Parekh
Primary Examiner
Technology Center 2800

DETAILED ACTION

Allowable Subject Matter

1. Claims 1-4, 6, 7, 18-20 and 22-24 are allowed.

Supplemental Examiner's Amendment

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

- A. Cancel claims 9 and 21.
- B. Claim 19, line 15: Delete "and"
- C. At the end of claim 19, line 17: Insert --- "and a plurality of electrode posts formed between said wiring patterns and said external terminals, wherein said sealing portion is formed such that a top surface of said electrode posts is exposed." ---.
- D. Claim 22, line 1: Delete "claim 21" and insert --- "claim 19" ---.

Authorization for this examiner's amendment was given in a telephone interview with Nick Bromer on 08-18-05.

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

The references of record do not teach either singularly or in combination at least the limitations "a base frame including a first surface, and a second surface which opposes said first surface, and including an opening portion formed through the base frame", "said semiconductor chip being disposed within said opening portion such that the level of said first main surface is substantially equal to the level of said first surface", "portions of said wiring patterns on a boundary and vicinity thereof between a region on the upper side of said semiconductor chip and the base frame being wider or thicker than other portions of said wiring patterns" and "a plurality of external terminals provided included over said wiring patterns in a region including the upper side of said base frame" in a resin sealed device having a semiconductor chip being disposed in the opening in a base frame, or "a silicon wafer constituting a base frame including a first surface, and a second surface which opposes said first surface, and including an opening portion formed through the base frame", "said semiconductor chip being disposed within said opening portion such that the level of said first main surface is substantially equal to the level of said first surface", and "a plurality of electrode posts formed between said wiring patterns and said external terminals, wherein said sealing portion is formed such that a top surface of said electrode posts is exposed" in a resin sealed device having a semiconductor chip being disposed in the opening in a silicon wafer and a plurality of wiring patterns being electrically connected to the respective electrode pads and external terminals.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Parekh whose telephone number is 571-272-1663. The examiner can normally be reached on 09:00AM-05:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on 571-272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAN or Public PAG. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAG system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). Any inquiry of a general nature or relating to the

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status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.



NP

NITIN PAREKH

12-28-05

PRIMARY EXAMINER

TECHNOLOGY CENTER 2800